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Inventor Information for 10/797254

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Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity/Reexam	Foreign E
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Inventor Name Search Result

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Last Name = OBERG

First Name = MATS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09576751	Not Issued	160	05/23/2000	Parity check outer code and runlength constrained outer code usable with parity bits	OBERG, MATS
09577552	6795947	150	05/24/2000	PARITY CHECK OUTER CODE AND RUNLENGTH CONSTRAINED OUTER CODE USABLE WITH PARITY BITS	OBERG, MATS
10053885	6661356	150	01/16/2002	METHOD AND APPARATUS FOR DC-LEVEL CONSTRAINED CODING	OBERG, MATS
10135422	6961197	150	04/29/2002	CORRECTING ERRORS IN DISK DRIVE READ BACK SIGNALS BY ITERATING WITH THE REED-SOLOMON DECODER	OBERG, MATS
10155777	6917313	150	05/24/2002	DC-FREE CODES	OBERG, MATS
10313651	Not Issued	61	12/06/2002	Correcting errors in disk drive read back signals by iterating with the Reed-Solomon decoder	OBERG, MATS
10393640	7107514	150	03/21/2003	METHOD AND APPARATUS FOR DETECTING VITERBI DECODER ERRORS DUE TO QUASI-CATASTOPHIC SEQUENCES	OBERG, MATS
10695531	6917314	150	10/28/2003	METHOD AND APPARATUS FOR DC-LEVEL CONSTRAINED CODING	OBERG, MATS
10737648	7116504	150	12/15/2003	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYSTEM	OBERG, MATS
10752817	Not Issued	89	01/06/2004	Method and apparatus to limit DC-level in coded data	OBERG, MATS

<u>10754325</u>	Not Issued	93	01/09/2004	THERMAL ASPERITY COMPENSATION IN PERPENDICULAR RECORDING	OBERG, MATS
<u>10765431</u>	Not Issued	93	01/26/2004	FILTER DRIVEN DC-FREE CODES	OBERG, MATS
<u>10797254</u>	Not Issued	41	03/10/2004	Method and system to improve decision-driven control loops	OBERG, MATS
<u>10819333</u>	Not Issued	133	04/06/2004	Thermal asperity detection for perpendicular magnetic recording	OBERG, MATS
<u>10862847</u>	Not Issued	94	06/07/2004	PARITY CHECK OUTER CODE AND RUNLENGTH CONSTRAINED OUTER CODE USABLE WITH PARITY BITS	OBERG, MATS
<u>10872098</u>	Not Issued	93	06/17/2004	CIRCUITS, ARCHITECTURES, SYSTEMS, METHODS, ALGORITHMS AND SOFTWARE FOR CONDITIONAL MODULATION CODING	OBERG, MATS
<u>10899630</u>	Not Issued	30	07/26/2004	DC-control for post processor	OBERG, MATS
<u>11090209</u>	<u>7274524</u>	150	03/28/2005	CORRECTING ERRORS IN DISK DRIVE READ BACK SIGNALS BY ITERATING WITH THE REED-SOLOMON DECODER	OBERG, MATS
<u>11166425</u>	Not Issued	93	06/24/2005	METHOD AND APPARATUS FOR DC-LEVEL CONSTRAINED CODING	OBERG, MATS
<u>11181084</u>	<u>7106223</u>	150	07/14/2005	DC-FREE CODES	OBERG, MATS
<u>11181401</u>	<u>7199731</u>	150	07/14/2005	DC-FREE CODES	OBERG, MATS
<u>11181545</u>	<u>7187308</u>	150	07/14/2005	DC-FREE CODES	OBERG, MATS
<u>11217349</u>	Not Issued	93	09/02/2005	CORRECTING ERRORS IN DISK DRIVE READ BACK SIGNALS BY ITERATING WITH THE REED-SOLOMON DECODER	OBERG, MATS
<u>11327684</u>	Not Issued	30	01/06/2006	Slicer bias loop	OBERG, MATS
<u>11446808</u>	Not Issued	30	06/05/2006	Method and apparatus to increase storage capacity on optical storage	OBERG, MATS

				medium	
<u>11463278</u>	<u>7262928</u>	150	08/08/2006	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYSTEM	OBERG, MATS
<u>11463280</u>	Not Issued	100	08/08/2006	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYSTEM	OBERG, MATS
<u>11516272</u>	Not Issued	41	09/06/2006	Method and apparatus for detecting viterbi decoder errors due to quasi-catastrophic sequences	OBERG, MATS
<u>11516364</u>	Not Issued	30	09/06/2006	Method and apparatus for detecting viterbi decoder errors due to quasi-catastrophic sequences	OBERG, MATS
<u>11546744</u>	Not Issued	25	10/11/2006	Write splice for optical recording channels	OBERG, MATS
<u>11713996</u>	Not Issued	30	03/05/2007	DC-free codes	OBERG, MATS
<u>11729747</u>	Not Issued	30	03/29/2007	Filter driven DC-free code	OBERG, MATS
<u>11746371</u>	Not Issued	17	05/09/2007	OFFSET LOOP FOR WOBBLE	OBERG, MATS
<u>11775119</u>	Not Issued	19	07/09/2007	METHOD AND DEVICE FOR INCREASED PULL-IN RANGE OF TIMING RECOVERY CIRCUIT FOR STORAGE DEVICES	OBERG, MATS
<u>11775757</u>	Not Issued	19	07/10/2007	TIMING LOOP BASED ON ANALOG TO DIGITAL CONVERTER OUTPUT AND METHOD OF USE	OBERG, MATS
<u>11836050</u>	Not Issued	17	08/08/2007	DC-OFFSET COMPENSATION LOOPS FOR MAGNETIC RECORDING SYSTEM	OBERG, MATS
<u>60158211</u>	Not Issued	159	10/07/1999	RUN-LENGTH-CONSTRAINED PARITY CHECK CODES FOR TURBO-EQUALIZED PARTIAL-RESPONSE CHANNELS	OBERG, MATS
<u>60273357</u>	Not Issued	159	03/05/2001	Apparatus and method for rate 32/33 DC-free code	OBERG, MATS
<u>60344407</u>	Not Issued	159	12/28/2001	Correcting errors in disk drive read back signals by iterating with the reed-solomon decoder	OBERG, MATS

60349895	Not Issued	159	01/16/2002	C7500 DC-free codes	OBERG, MATS
60352756	Not Issued	159	01/28/2002	Correcting errors in disk drive read back signals by iterating with the reed-solomon decoder	OBERG, MATS
60352776	Not Issued	159	01/28/2002	C7555 DC-free codes	OBERG, MATS
60406259	Not Issued	159	08/27/2002	Method and apparatus to detect early decision viterbi errors due to quasi-catastrophic sequences	OBERG, MATS
60457613	Not Issued	159	03/25/2003	DC-offset compensation loops for magnetic recording system	OBERG, MATS
60472073	Not Issued	159	05/20/2003	DC-compensation loop	OBERG, MATS
60478869	Not Issued	159	06/16/2003	Method and apparatus to limit DC-level in coded data	OBERG, MATS
60485216	Not Issued	159	07/07/2003	Scrambling to reduce DC-content in encoded data	OBERG, MATS
60487693	Not Issued	159	07/16/2003	Baseline compensation during thermal asperity	OBERG, MATS
60513688	Not Issued	159	10/22/2003	Method and apparatus to improve decision driven control loops	OBERG, MATS
60514519	Not Issued	159	10/24/2003	TA-detection for perpendicular magnetic recording	OBERG, MATS

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
60529046	Not Issued	159	12/12/2003	Conditional modulation codes	OBERG, MATS
60536789	Not Issued	159	01/15/2004	DC-control for post processor	OBERG, MATS
60702164	Not Issued	159	07/25/2005	Slicer bias loop	OBERG, MATS
60703347	Not Issued	159	07/28/2005	NATP alternatives	OBERG, MATS
60734444	Not Issued	159	11/08/2005	Method and apparatus to increase storage capacity on optical storage medium	OBERG, MATS
60779950	Not Issued	159	03/07/2006	Write splice for optical recording	OBERG, MATS
60799586	Not Issued	159	05/11/2006	Offset loop for wobble	OBERG, MATS
60822312	Not Issued	159	08/14/2006	Servo Optimization for Optical Storage Devices Using Viterbi Margin Metrics	OBERG, MATS
60823731	Not Issued	159	08/28/2006	Servo Optimization for Optical Storage Devices Using Viterbi Margin Metrics	OBERG, MATS
60830628	Not Issued	159	07/13/2006	ADC output based timing loop	OBERG, MATS
60830630	Not Issued	159	07/13/2006	Timing loop with large pull-in range	OBERG, MATS
60863487	Not Issued	20	10/30/2006	Method and Apparatus to Correct Wobble Phase Slip in Optical Recorders	OBERG, MATS
60886790	Not Issued	20	01/26/2007	Limit Equalizer Output Based Timing Loop	OBERG, MATS
60887032	Not Issued	20	01/29/2007	Limit Equalizer Output Based Timing Loop	OBERG, MATS

60896139	Not Issued	20	03/21/2007	Decoupling of Asymmetry from Baseline Compensation	OBERG, MATS
60912313	Not Issued	20	04/17/2007	Asymmetry Compensation Method	OBERG, MATS
60914473	Not Issued	20	04/27/2007	Method for Radial Tilt Calibration of Optical Storage Systems	OBERG, MATS
60955938	Not Issued	20	08/15/2007	All-Digital Jitter Measurement Circuit	OBERG, MATS

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